



Addendum to Sentinel white papers: DAC Operation.

- 1.0 In order to exercise the Sentinel DAC the following considerations must be applied:
- 2.0 The real registers (0 thru 5) must have finite binary values loaded into them.
- 3.0 Before using the DAC a delay time must be set. Address for delay register is 0110. Delay is a 5 bit value. The amount of time the DAC stays on is:

$$(\text{Value set in the delay register} - 12) * 2(\text{oscillator time period}).$$
- 4.0 When using SRC1 as the DAC output, the registers 0001, 0011 and 0101 must be used. When using SRC2 the source registers 0000, 0010 and 0100 must be used. [e.g READ command is 101001 for SRC1 and 101000 for SRC2.]
- 5.0 The DAC will generate a current proportional to the value stored in the appropriate source register, the value of the resistor from REG to ground and the voltage reference value.

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